

IN THE CLAIMS:

Please amend Claims 8, 9, and 11-15 and cancel Claim 10. The following is a complete listing of the claims, reflects all changes currently being made thereto, and replaces all prior versions and listings of claims in the present application:

1.-7. (Cancelled)

8. (Currently Amended) A liquid crystal apparatus, comprising:

a liquid crystal device; and

a drive means for driving the liquid crystal device.

wherein the liquid crystal device comprises comprising an active matrix substrate having thereon a plurality of signal lines arranged in columns, a plurality of scanning lines arranged in rows, [[and]] pixel electrodes each connected via a pixel switch to an intersection of the signal lines and the scanning lines so as to supply positive and negative polarity picture signals to the pixel electrodes via the signal lines, a counter substrate disposed opposite to the active matrix substrate, and a liquid crystal disposed between the active matrix substrate and the counter substrate, and

wherein the drive means for driving the liquid crystal device; ~~wherein said drive means~~ includes:

a first common signal line for supplying only the positive polarity picture signals to each of the plurality of signal lines,

a second common signal line for supplying only the negative polarity picture signals to each of the plurality of signal lines,

a first transfer switch for connecting one signal line with the first common signal line for selectively supplying only the positive polarity picture signals to the one signal line of the plurality of signal lines, and

a second transfer switch for connecting the one signal line with the second common signal line for selectively supplying only the negative polarity picture signals to the one signal line, wherein the one signal line is connected to the first transfer switch and the second transfer switch,

wherein the first transfer switch comprises a first transistor of a p-channel type and the second transfer switch comprises a second transistor of an n-channel type; and

column wherein the liquid crystal apparatus further comprises an inversion drive means for[[[:]]], in a first frame, selectively turning on the first transfer switch for the one signal line, and in a second frame, selectively turning on the second transfer switch for the one signal line.

9. (Withdrawn, Currently Amended) A liquid crystal apparatus, comprising:

a liquid crystal device; and

a drive means for driving the liquid crystal device.

wherein the liquid crystal device comprises comprising an active matrix substrate having thereon a plurality of signal lines arranged in columns, a plurality of scanning lines arranged in rows, [[and]] pixel electrodes each connected via a pixel switch to an intersection of the signal lines and the scanning lines so as to supply positive and negative polarity picture signals to the pixel electrodes via the signal lines, a counter substrate disposed opposite to the active matrix substrate, and a liquid crystal disposed between the active matrix substrate and the counter substrate, and

wherein the drive means for driving the liquid crystal device;~~wherein said drive means~~ includes:

a first common signal line for supplying only the positive polarity picture signals to each of the plurality of signal lines,

a second common signal line for supplying only the negative polarity picture signals to each of the plurality of signal lines,

a first transfer switch for connecting one signal line of the plurality of signal lines with the first common signal line for selectively supplying only the positive polarity picture signals to the one signal line of the plurality of signal lines, and

a second transfer switch for connecting the one signal line with the second common signal line for selectively supplying only the negative polarity picture signals to the one signal line, wherein the one signal line is connected to the first transfer switch and the second transfer switch,

wherein the first transfer switch comprises a first transistor of a p-channel type and the second transfer switch comprises a second transistor of an n-channel type; and

wherein the liquid crystal apparatus further comprises a dot inversion drive means for[[[:]], in a first frame, selectively turning on the first transfer switch for the one signal line at a first timing, and selectively turning on the second transfer switch for the one signal line at a second timing different from the first timing[[[:]], and in a second frame, selectively turning on the second transfer switch for the one signal line at a third timing, and selectively turning on the first transfer switch for the one signal line at a fourth timing different from the third timing.

10. (Cancelled)

11. (Currently Amended) [[A]] The liquid crystal apparatus according to Claim 8, further comprising a picture signal-supplying means including a first picture signal-generating means for

generating the positive polarity picture signals supplied to the first common signal line and a second picture signal-generating means for generating the negative polarity picture signals supplied to the second common signal line, wherein the first picture signal-generating means generates the positive polarity picture signals in a range between a highest voltage and a central voltage supplied to the pixel electrodes; the second picture signal-generating means generates the negative polarity picture signals in a range between the central voltage and a lowest voltage supplied to the pixel electrodes; the first picture signal-generating means is operated at a first supply voltage and the second ~~[[first]]~~ picture signal-generating means is operated at ~~[[the]]~~ a second supply voltage different from the first supply voltage; the first supply ~~voltages are~~ voltage is set to be ~~[[the]]~~ a voltage higher than the highest voltage by ~~by~~ $[[+]]$ α and a voltage lower than the central voltage by ~~by~~ $[-]$ α ; and the second supply ~~voltages are~~ voltage is set to be ~~[[the]]~~ a voltage higher than the central voltage by ~~by~~ $[[+]]$ α and a voltage lower than the lowest voltage by ~~by~~ $[-]$ α , wherein the α denotes ~~[[α]]~~ a voltage lowering margin due to an internal resistance in the first picture signal-generating means or in the second picture signal-generating means.

12. (Currently Amended) ~~[[A]]~~ The liquid crystal apparatus according to Claim 11, wherein the α is in ~~[[the]]~~ a range of 0 volt to 1 volt.

13. (Currently Amended) ~~[[A]]~~ The liquid crystal apparatus according to Claim 11, wherein the first and second transfer switches and the picture signal-supplying means are disposed on the active matrix substrate.

14. (Currently Amended) ~~[[A]] The~~ liquid crystal apparatus according to Claim 13, wherein the active matrix substrate comprises an insulating substrate.

15. (Currently Amended) ~~[[A]] The~~ liquid crystal apparatus according to Claim 13, wherein the active matrix substrate comprises a single crystal substrate.